

## WHAT IS CLAIMED IS:

1. A data buffer circuit for a video decoder comprising:  
a receiver circuit adapted to receive a video bitstream;  
a ring buffer adapted to store the video bitstream; and  
an error resilience module adapted to retrieve data from the ring buffer.
2. The data buffer circuit as defined in Claim 1, wherein the receiver circuit comprises a wireless receiver.
3. The data buffer circuit as defined in Claim 1, further comprising a log interface circuit adapted to store data logging information in the ring buffer such that the data logging information is aligned with corresponding data from the video bitstream.
4. The data buffer circuit as defined in Claim 1, further comprising a VOP decoder disposed in a data flow between the receiver circuit and the ring buffer such that the video bitstream stored by the ring buffer is in a decoded form.
5. A data buffer circuit for a video decoder comprising:  
means for receiving a video bitstream;  
means for inspecting the video bitstream for error;  
means for storing the video bitstream in a ring buffer regardless of an error indication;  
means for storing data logging information corresponding to video bitstream data in the ring buffer in an aligned manner with the corresponding video bitstream data; and  
means for automatically retrieving both a portion of the video bitstream and a corresponding portion of the data logging information from the ring buffer in response to a request for data.
6. A method of accessing information from a video bitstream comprising:  
receiving a video bitstream;  
inspecting the video bitstream for error;  
storing the video bitstream in a ring buffer regardless of an error indication;  
storing data logging information corresponding to video bitstream data in the ring buffer in an aligned manner with the corresponding video bitstream data; and

automatically retrieving both a portion of the video bitstream and a corresponding portion of the data logging information from the ring buffer in response to a request for data.

7. The method as defined in Claim 6, further comprising wirelessly receiving the video bitstream.

8. The method as defined in Claim 6, further comprising receiving the video bitstream in an MPEG-4 compliant decoder.

9. The method as defined in Claim 6, further comprising decoding video object planes (VOPs) from the video bitstream prior to storing the video bitstream in the ring buffer, and wherein the storing of the video bitstream comprises storing the decoded VOPs.

10. The method as defined in Claim 6, further comprising decoding video object planes (VOPs) from the video bitstream after the video bitstream has been stored in the ring buffer.